

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Inventor(s): ELLIS, Frampton  
 Atty.Dkt: 081498/0274516  
 Appln. No.: Unknown/  
 Filed: Herewith  
 FOR: GLOBAL NETWORK COMPUTERS

Group Art Unit: Unknown  
 Examiner: Unknown

jc872 U.S. PRO  
 09/935779  
 08/24/01  
 #5  
 4-1-02  
 8M

Date: August 24, 2001

**IDS LETTER CITING APPLICATION(S)  
 AND ATTACHING PTO-1449/DOCUMENTS**

The Examiner's attention is directed to the following application(s) of:

Examiner's Initials*	FIRST INVENTOR (Last Name Only)	APPLICATION NO. & Filing Date	ENCLOSED
	ELLIS	09/884,041, filed June 20, 2001	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/669,730, filed September 26, 2000	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/571,558, filed May 16, 2000	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/320,660, filed May 27, 1999	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/315,026, filed May 20, 1999	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/085,755, filed May 27, 1998	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	08/980,058, filed November 26, 1997	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/213,875, filed December 17, 1998	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other

**PLEASE DO NOT PRINT** the above information on the patent which results from the subject application.

Consideration of each listed application is earnestly solicited since unpublished patent applications are contemplated as IDS material; see the exception in Rule 98(a)(2)(iii) and note the penultimate sentence of MPEP 609.

Further, in keeping with MPEP 609, Subsec. C(2), 2nd para., line 10 to end of the paragraph (especially note lines 18-25) **PLEASE RETURN A COPY OF THIS LETTER** with the Examiner's initials adjacent each above listing so that applicant will know that each listed application has been considered as required by PTO policy. Thanks.

ELLIS - New Application

Secondly, please consider each document which is listed on the attached Form PTO-1449 and return a copy of that form with the Examiner's initials adjacent each citation.

Pursuant to the exception in 37 CFR 1.98 (d), copies of the above-listed applications and documents are not submitted herewith.

Thanks again.

Respectfully submitted,

*Carlo Cotroni* P48,715  
for  
Dale S. Lazar

\*The Examiner's initials adjacent a citation indicates he/she has considered the cited application relative to the subject application.

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

Date: August 24, 2001 Page 1 of 6 Examiner: Unknown Group Art Unit: Unknown

## U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR 4,827,508	05/1989	SHEAR			
	BR 4,918,596	04/1990	NAKANO			
	CR 4,969,092	11/1990	SHORTER			
	DR 5,025,369	06/1991	SCHWARTZ			
	ER 5,031,089	07/1991	LIU et al.			
	FR 5,103,393	04/1992	HARRIS et al.			
	GR 5,109,329	04/1992	STRELIOFF			
	HR 5,109,512	04/1992	BAHR et al.			
	IR 5,136,708	08/1992	LAPOURTE et al.			
	JR 5,155,808	10/1992	SHIMIZU			
	KR 5,291,494	03/1994	BRUCKERT et al.			
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	XR 5,550,984	08/1996	GELB			
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	BR 5,572,643	11/1996	JUDSON			
	ZR 5,586,121	12/1996	MOURA et al.			
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	AAR 5,594,491	01/1997	HODGE et al.			
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	CCR 5,615,127	03/1997	BEATTY et al.			
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	EER 5,678,028	10/1997	BERSHTEYN et al.			
	DR 5,680,548	10/1997	TRUGMAN			
	ER 5,696,902	12/1997	LECLERCQ et al.			
	FFR 5,710,884	01/1998	DEDRICK			
	GGR 5,748,489	05/1998	BEATTY et al.			

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FORM PTO-1449 (modified) To: U.S. Department of Commerce (PW FORM PAT-1449) Patent and Trademark Office				Atty. Dkt. No.	M#	Client Ref.
					0274516	GNC22US
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Applicant: ELLIS		
				Application No.: Unknown		

Date: August 24, 2001	Page	2	of	6		Examiner: Unknown	Group Art Unit: Unknown
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**U.S. PATENT DOCUMENTS**

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
AR	5,758,077	05/1998	DANAHY et al.			
BR	5,758,345	05/1998	WANG			
CR	5,761,507	06/1998	GOVETT			
FR	5,774,668	06/1998	CHOQUIER et al.			
DR	5,790,431	08/1998	AHRENS, Jr. et al.			
GR	5,793,968	08/1998	GREGERSON et al.			
ER	5,794,059	08/1998	BARKER et al.			
HR	5,815,665	09/1998	TEPER et al.			
IR	5,815,793	09/1998	FERGUSON			
JR	5,826,014	10/1998	COLEY et al.			
KR	5,828,833	10/1998	BELVILLE et al.			
LR	5,844,594	12/1998	FERGUSON			
FR	5,845,074	12/1998	KOBATA			
GR	5,850,449	12/1998	MCMANIS			
HR	5,864,738	01/1999	KESSLER et al.			
MR	5,881,284	03/1999	KUBO			
NR	5,889,989	03/1999	ROBERTAZZI et al.			
OR	5,909,681	06/1999	PASSERA et al.			
PR	5,930,511	07/1999	HINSLEY			
QR	5,964,832	10/1999	KISOR			
RR	5,978,829	11/1999	CHUNG et al.			
SR	6,052,555	04/2000	FERGUSON			
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VR	6,112,243	08/2000	DOWNS et al.			

FOREIGN PATENT DOCUMENTS						English Abstract	Translation Readily Available
	Document Number	Date MM/YYYY	Country	Inventor Name			
WR	WO 98/26366	06/1998	WIPO	Ellis			
XR	0 853 279	07/1998	EUROPE	Nagaratina et al.			
YR	WO 94/01964	01/1994	WIPO	Craig			
ZR	WO 95/01060	01/1995	WIPO	Kramer et al.			
AAR	EP 0 840216A2	05/1998	Europe	Chung et al.			

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BBR	"Ein-Chip-Firewall," Elektroniknet Top News, XP-002164257		

Examiner	Date Considered:
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		Application No.: Unknown	

Date: August 24, 2001 Page 3 of 6 Examiner: Unknown Group Art Unit: Unknown

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AR	"Means for Implementing Optical Interconnections for Parallel Processors," IBM Technical Disclosure Bulletin, April 1991, Vol. 33, No. 11, pages 56-58, XP 000110310		
BR	Alexandrov et al., "SuperWeb: Research Issues in Java-Based Global Computing," Concurrency, Vol. 9, No. 6, June 1997, pages 535-553		
CR	Baratloo et al., "Charlotte: Metacomputing on the Web," 9th International Conference on Parallel and Distributed Computing Systems (PDCS), 1996, pages 1-8		
DR	Bevinakoppa et al., "Digital Image Compression on a Network of Transputers," Proc. of 5 <sup>th</sup> Australian Transputer & OCCAM User Group Conference, IOS Press, Amsterdam, pp. 25-32		
ER	Blumofe, R. et al., "Scheduling Large-Scale Parallel Computations on Networks of Workstations," Proc. Of the 3 <sup>rd</sup> IEEE Int'l Symp on High Performance Distributed Computing, pp. 96-105, Aug. 1994		
FR	Brecht et al., "ParaWeb: Towards World-Wide Supercomputing," Proceedings of the Seventh AcM SIGOPS European Workshop, September 1996, 8 pages		
GR	Brisbin, "Shopping for Internet access," MacUser, December 1994, v.10, p. 133(2)		
HR	Brown et al., Special Edition Using Netscape <sup>TM</sup> , 2 Second Edition, Que Corporation, 1995, Ch. 1-2		
IR	Capello et al., "Market-Based Massively Parallel Internet Computing," Proceedings Third Working Conference on Massively Parallel Programming Models, 1998, pages 118-129		
JR	Celenk, M. et al., "Parallel Task Execution in LANs and Performance Analysis," Proc. Of the 1995 IEEE 14 <sup>th</sup> Annual Int'l Phoenix Conf. On Computers and Communications, pp. 423-429, Mar. 1995		
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MR	Fogg, C., "Survey of software and hardware VLC architectures," SPIE, vol. 2186, Image and Video Compression (1994), pp. 29-37		
NR	Fox et al., "Towards Web/Java based High Performance Distributed Computing-an Evolving Virtual Machine," <a href="http://www.npac.syr.edu/projects/webspace/doc/hpdc5/paper">www.npac.syr.edu/projects/webspace/doc/hpdc5/paper</a> , June 10, 1996, 11 pages		
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PR	Fox, E., "Advances in Interactive Digital Multimedia Systems," Computer, Oct. 1991, pp. 9-21		
QR	Gemmell et al., "Multimedia Storage Servers: a Tutorial," Computer, May 1995, pp. 40-49		
RR	Geoffrey C. Fox and Wojtek Furmanski, Petaops and Exaops: Supercomputing on the Web, "IEEE INTERNET COMPUTING," Vol. 1, Number 2 March-April 1997, pgs. 38-46.		

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Group Art Unit: Unknown			

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DR	Hare et al., "Master the Complexities of Network Security," Internet Firewalls and Network Security, Second Edition, pp. 325-350 and 516		
ER	Hayes, "Computing Science: Collective Wisdom," American Scientist, March-April 1998, pages 1-8		
FR	Kim, B., "ATM Network: Goals and Challenges," Communications of the ACM, Feb. 1995, vol. 38, No. 2, pp. 39-44, 109		
GR	Kivanc Dincer and Geoffrey C. Fox, Building a World-Wide Virtual Machine Based on Web and HPCC Technologies, "STUDENT TECHNICAL PAPERS," <a href="http://www.supercomp.org/sc96/proceedings/SC96PROC/DINCER/INDEX.HTM">http://www.supercomp.org/sc96/proceedings/SC96PROC/DINCER/INDEX.HTM</a> pgs. 1-18.		
HR	Kremien, O., "Buying and Selling Computational Power Over the Network," Proc. Of the 4 <sup>th</sup> Int'l. Conf. On Computer Communications and Networks, IEEE, pp. 616-619, Sep. 1995		
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KR	Litzkow et al., "Condor-A Hunter of Idle Workstations," Proc. Of the 8 <sup>th</sup> Int'l. Conf. On Distributed Computing Systems, IEEE, pp. 104-111, Jun. 1998		
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MR	Markoff, "A New Standard to Govern PC's with Multiple Chips, Work Stations Mimicking Supercomputers," The New York Times, October 28, 1997		
NR	McHenry et al., "An FPGA-Based Coprocessor for ATM Firewalls," Field-Programmable Custom Computing Machines 1997, 16-18 April 1997, pages 30-39, XP-002157218		
OR	Morris, J., et al., "Fault Tolerant Networks of Workstations," Proc. Of the 3 <sup>rd</sup> Int'l. Conf. On High Performance Computing, IEEE, pp. 271-276, Dec. 1996		
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TR	Nass, R., "Hardware-software combo could simplify MPEG real-time video compression," Electronic Design, May 3, 1993, p. 36		
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WR	Ozer, J., "Why MPEG is Hot," PC Magazine, Apr. 11, 1995, pp. 130-131		

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BR	Qiao et al., "Time-Division Optical Communications in Multiprocessor Arrays," ACM, 1991, pages 644-653, XP 000337522			
CR	Rincon et al., "The Changing Landscape of System-on-a-Chip Design," MicroNews, Third Quarter 1999, <a href="http://www.chips.ibm.com/micronews/vol5_no3/rincon.html">www.chips.ibm.com/micronews/vol5_no3/rincon.html</a> , pages 1-10, Vol. 5, No. 3			
DR	Sachs, M. et al., "LAN and I/O Convergence: A Survey of the Issues," Computer, Dec. 1994, pp. 24-32			
ER	Sakano et al., "A Three-Dimensional Mesh Multiprocessor System Using Board-to-Board Free-Space Optical Interconnects: COSINE-III," IEEE, 1993, pages 278-283, XP 000463415			
FR	Schroeder, E., "New offerings buoy MPEG as video standard," Desktop Computing, PC Week, May 8, 1995, pp. 1&29			
GR	Shen et al., "A Parallel Implementation of an MPEG1 Encoder: Faster than Real-Time!", Proc. Of SPIE Conf. On Digital Video Compression, San Jose, CA, Feb. 1995			
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IR	Steven M. Bellovin and William R. Cheswick, Network Firewalls, "IEEE Communications Magazine 1994," pgs. 50-57.			
JR	Sullivan et al., "A New Major SETI Project Based on Project Serendip Data and 100,000 Personal Computers." <a href="http://setiathome.berkeley.edu/woody_paper.htm/">http://setiathome.berkeley.edu/woody_paper.htm/</a>			
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LR	Taisuke Boku, Hiroshi Nakamura, Kisaburo Nakazawa, and Yoichi Iwasaki, The Architecture of Massively Parallel Processor CP-PACS, "IEEE 1997," pgs. 31-40.			
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OR	Vetter, R., "ATM Concepts, Architectures, and Protocols," Communications of the ACM, Feb. 1995; vol. 38, No. 2, pp. 30-38, 109			
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QR	Waldspurger et al., "Spawn: A Distributed Computational Economy," IEEE Transactions on Software Engineering, Vol. 18, No. 2, February 1992, pages 103-117, XP-002124500			
RR	Weiyi et al., "Java-to-Go - Iterative Computing Using Java," September 10, 1996 <a href="http://ptolemy.eecs.berkeley.edu/dgm/javatools/java-to-go/">http://ptolemy.eecs.berkeley.edu/dgm/javatools/java-to-go/</a>			
SR	Yoon-Hwa Choi and Yu-Seok Kim, A Diagnostic Network for Massively Parallel Processing Systems, "IEEE 1994," pgs. 348-353.			

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Application No.: Unknown

Date: August 24, 2001 Page 6 of 6 Examiner: Unknown Group Art Unit: Unknown

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

AR	Yoshida, J., "MPEG-2 encoders make show of force at NAB," Electronic Engineering Times, Apr. 10, 1995			
BR	Yu, Y. et al., "Software Implementation of MPEG-II Video Encoding Using Socket Programming in LAN," SPIE vol. 2187, pp. 229-240, 1994			
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